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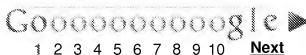
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An improved **Domino** adder circuit has a **carry** evaluating logic, including a **precharge transistor**, an evaluation **transistor**, and three **carry** evaluating paths ...

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is across the high-Vt PMOS **precharge transistor** which is tuned ... evaluate LB-**Domino**. The **carry** generation circuit is pure ...

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Dynamic pass-**transistor** dot operators for efficient parallel ...

fit of using NMOS **transistors** as **precharge transistors** is that ... Stone **carry** trees have been designed using **domino** logic. ...

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generate/propagate logic, the **carry-check** logic, the. **precharge** logic, and the appropriate **precharge** the overhead of extra **precharge** PMOS **transistors**. ...

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pass **transistor** logic or **carry** save techniques [131. **precharge** and keeper **transistors**. Peak power.

consumption in CD **domino** circuits is less than in ...

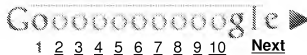
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[PPT] 9.5.1 **Domino** logic

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Adding an inverter and a **precharge transistor** results in the two outputs ... in several important computational algorithms such as the **carry** look-ahead adder.

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